


Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

|   |   |    |   |                          |                     |
|---|---|----|---|--------------------------|---------------------|
| <p>Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.</p> <p>See the date for form 1449A/PTO</p> <p><b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b></p> <p><i>(use as many sheets as necessary)</i></p> |   |    |   | <b>Complete if Known</b> |                     |
|   |   |    |   | Application Number       | 09/902,622          |
|   |   |    |   | Filing Date              | July 12, 2001       |
|   |   |    |   | First Named Inventor     | Jun KOYAMA, et al   |
|   |   |    |   | Group Art Unit           | 2672                |
|   |   |    |   | Examiner Name            | Anthony J. Blackman |
|   |   |    |   | Attorney Docket Number   | 0756-2339           |
| Sheet   | 1 | of | 3 |                          |                     |

[illegible][illegible]

| OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS |                       |   |                |
|---|-----------------------|---|----------------|
| Examiner Initials*                                | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
| nk  |                       | M. Bonnel et al., IEEE Electron Dev. Lett., 14(12) (1993) 551 "...TFTs with two step annealing process"   |                |
|   |                       | I. Fujieda et al., IEDM, '93 Proc., page 587, "Self-referenced Polys-Si TFT amplifier..."   |                |
|   |                       | M. Fuse et al., Sol. St. Phenom, 37-38 (1994) 565, "Performance of poly-si TFTs..."   |                |

|                       |   |                    |         |
|-----------------------|---|--------------------|---------|
| Examiner<br>Signature |  | Date<br>Considered | 2/22/06 |
|-----------------------|---|--------------------|---------|

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

**Burden Hour Statement:** This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Commissioner for Patents, Washington, DC 20231: